

Docket No.: M4065.0151-B
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Vishnu K. Agarwal, et al.

Application No.: Not Yet Assigned

Group Art Unit: 2814

Filed: August 17, 2001

Examiner: T. Doan

For: MULTILAYER ELECTRODE FOR A
FERROELECTRIC CAPACITOR

FIRST PRELIMINARY AMENDMENT

Box Non-Fee Amendment
Commissioner for Patents
Washington, DC 20231

Dear Sir:

Prior to examination on the merits, please amend the above-identified U.S. patent application as follows:

In the Specification

At page 1, before "BACKGROUND OF THE INVENTION," please insert the following new paragraph:

This patent application is a divisional application of U.S. patent application serial number 09/310,408, filed on May 12, 1999, entitled MULTILAYER ELECTRODE FOR A FERROELECTRIC CAPACITOR.

In the Claims

Please cancel claims 1-54.

REMARKS/ARGUMENTS

This application is a divisional application of U.S. patent application serial number 09/310,408, filed with the U.S. Patent and Trademark Office on May 12, 1999, entitled MULTILAYER ELECTRODE FOR A FERROELECTRIC CAPACITOR. Favorable action on this application is solicited.

Dated: August 17, 2001

Respectfully submitted,

By 

Thomas J. D'Amico

Registration No.: 28,371

DICKSTEIN SHAPIRO MORIN &

OSHINSKY LLP

2101 L Street NW

Washington, DC 20037-1526

(202) 828-2232

Attorneys for Applicant

09930953 081701
T.D.T.D. 05602660